



COFC
PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Takashi KOBAYASHI et al.

U.S. Patent No.: 6,894,319 B2

Issue Date: May 17, 2005

Serial No.: 10/020,573

Filed: October 31, 2001

Title: VERTICAL INSULATED GATE
SEMICONDUCTOR DEVICE

Attorney Docket No.: FUJI:200

Certificate of Mailing

I hereby certify that this paper is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on:

DATE: 06/07/05

BY: *[Signature]*
MARC A. ROSSI

Attention Certificate of Corrections Branch
COMMISSIONER FOR PATENTS
P.O. BOX 1450
ALEXANDRIA, VA 22313-1450

Certificate
JUN 13 2005
of Correction

REQUEST FOR CERTIFICATE OF CORRECTION

Sir:

Applicants request that a Certificate of Correction be issued to correct a errors that appear in the above-referenced patent due to a mistake made on the part of the Office, namely, the Title is incorrect and the dates of the first two listed Foreign Application Priority documents are incorrect. Applicants request that a Certificate of Correction be issued to show the Title as indicated on the Notice of Allowance and show the Priority documents with the correct Priority dates.

However, in the event that it should be determined that the error is not the fault of the Office, the Commissioner is authorized to charge deposit account 18-2056 any fees associated with the issuance of the Certificate of Correction.

Respectfully submitted,

ROSSI, KIMMS & McDOWELL LLP

06/07/05
DATE

[Signature]
MARC A. ROSSI
REG. NO. 31,923

P.O. Box 826
ASHBURN, VA 20146-0826
703-726-6020 (PHONE)
703-726-6024 (FAX)

JUN 13 2005

**UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION**

Page 1 of 1

PATENT NO. : 6,894,319 B2

APPLICATION NO.: 10/020,573

ISSUE DATE : May 17, 2005

INVENTOR(S) : Takashi KOBAYASHI et al.

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

The title shown is incorrect and should read:

--VERTICAL INSULATED GATE SEMICONDUCTOR DEVICE--

In the Section labeled Foreign Application Priority Data, the dates of the first two listed Priority Applications are incorrect and should be shown as follows:

--October 31, 2000 JP 2000-331840

November 28, 2000 JP 2000-361106--

MAILING ADDRESS OF SENDER (Please do not use customer number below):

ROSSI, KIMMS & McDOWELL LLP
P.O. BOX 826
ASHBURN, VA 20146-0826

This collection of information is required by 37 CFR 1.322, 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: **Attention Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.**

If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.

JUN 13 2005